

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10015414	FILING DATE 12/12/2001	CLASS 711	SUBCLASS 185.28	GAU 2485	EXAMINER PHAM
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**CONTINUING DATA VERIFIED: no LP

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** FOREIGN APPLICATIONS VERIFIED: yes HP
TAIWAN 90120931 12/04/2001

PG-PUB <input type="checkbox"/>	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no
ATTORNEY DOCKET NO JCLA7737		
TITLE : Method for programming and erasing non-volatile memory with nitride tunneling layer U.S. DEPT. OF COM/USPTO-TM-PTO-436L(Rev. 12-2001)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Total Claims	Print Claim for O.G.	
Assistant Examiner				
Primary Examiner		DRAWING		
PREPARED FOR ISSUE		Sheets Drawg.	Figs. Drawg.	Print Fig.
Application Examiner				
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